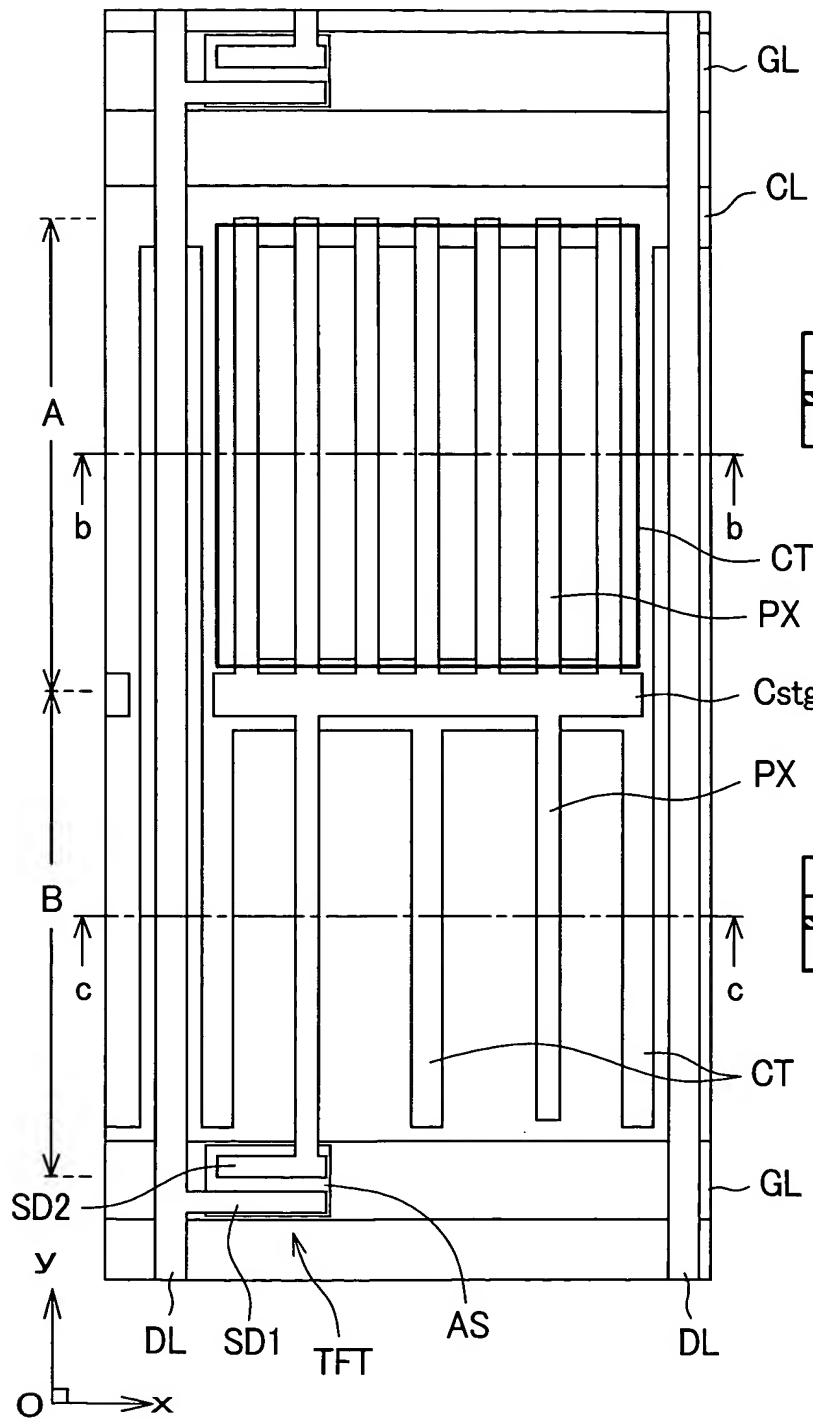
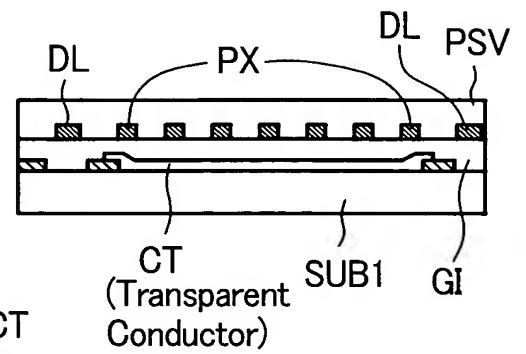


*FIG. 1A*



*FIG. 1B*



*FIG. 1C*

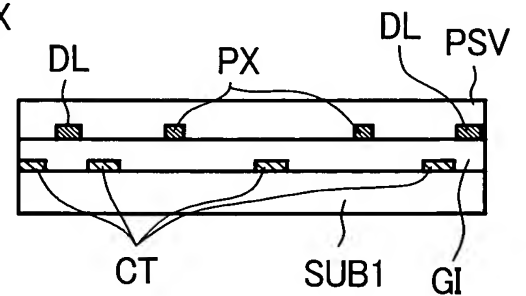
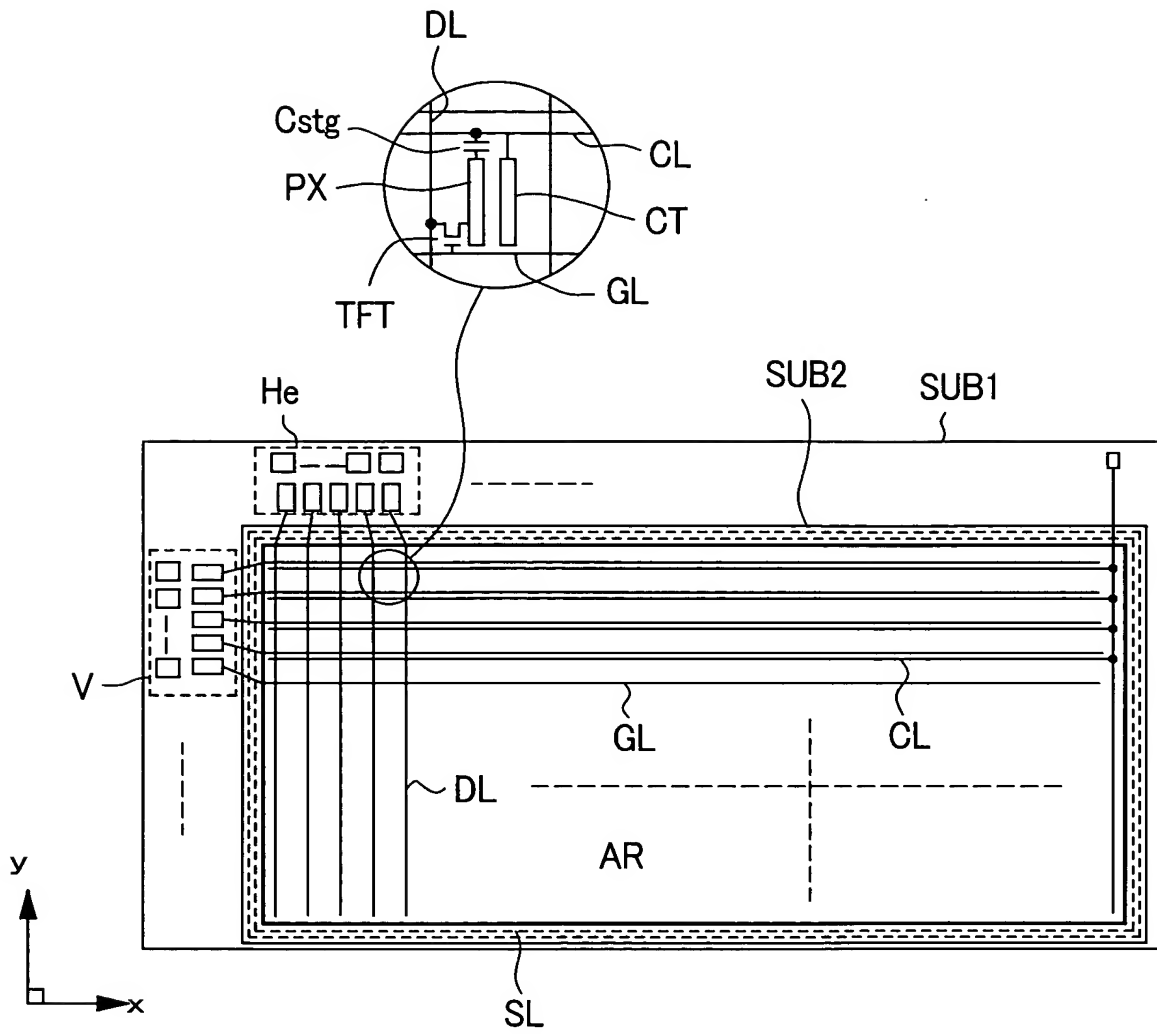
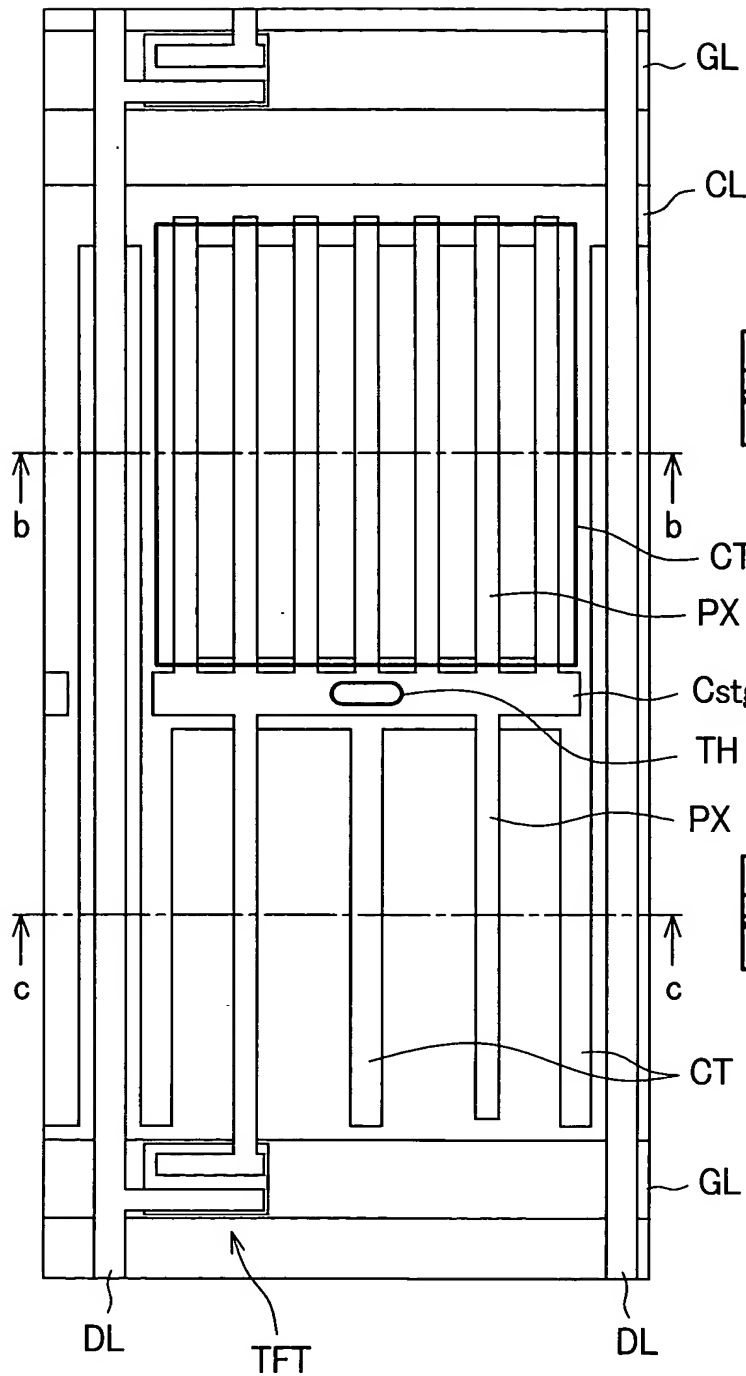


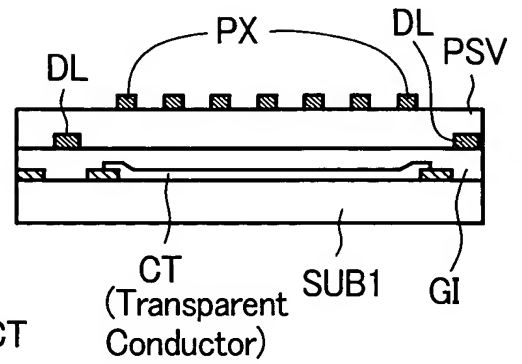
FIG. 2



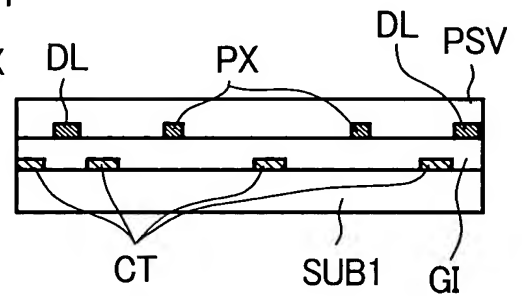
*FIG. 3A*



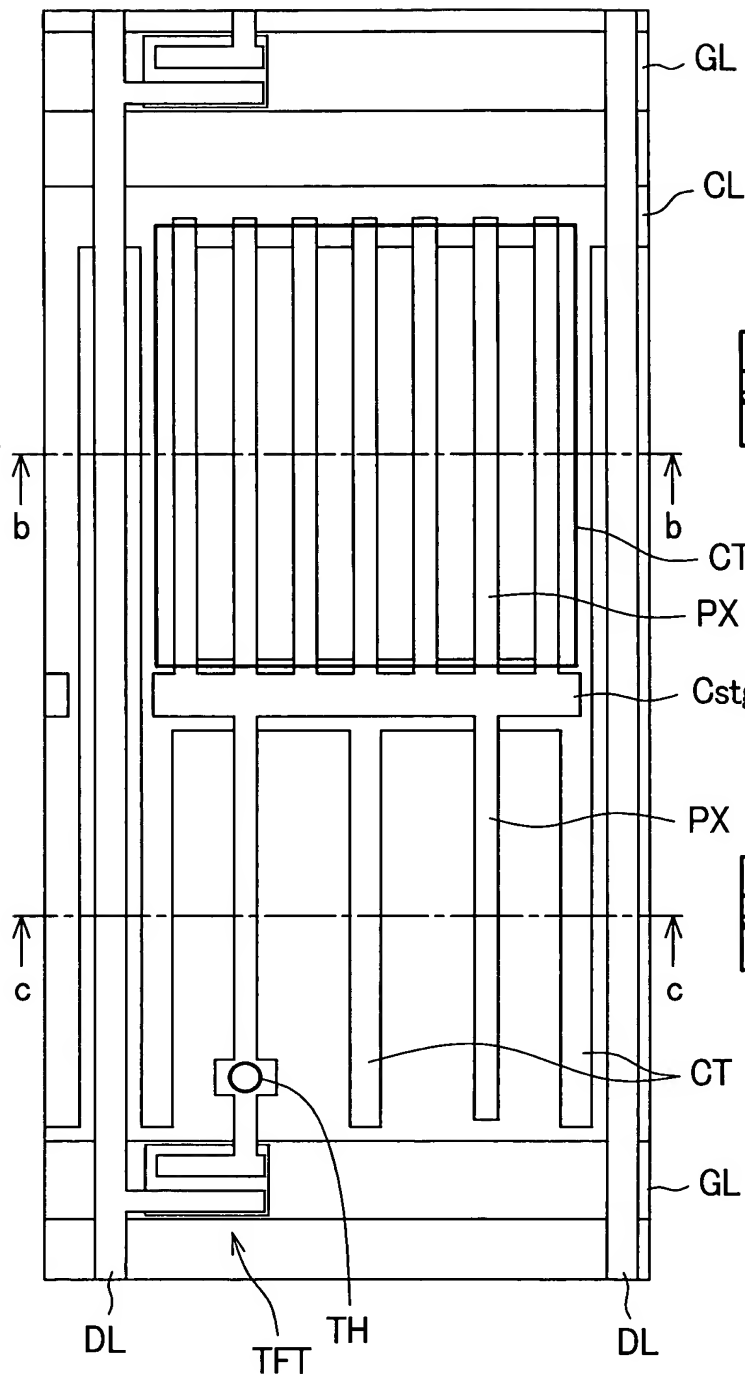
*FIG. 3B*



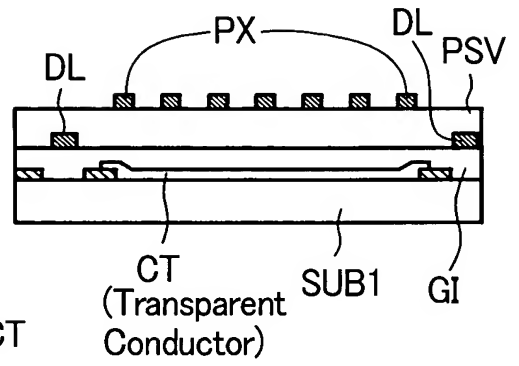
*FIG. 3C*



*FIG. 4A*



*FIG. 4B*



*FIG. 4C*

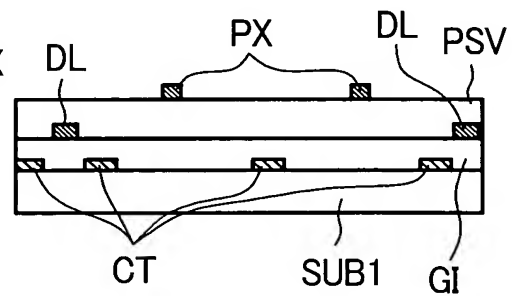
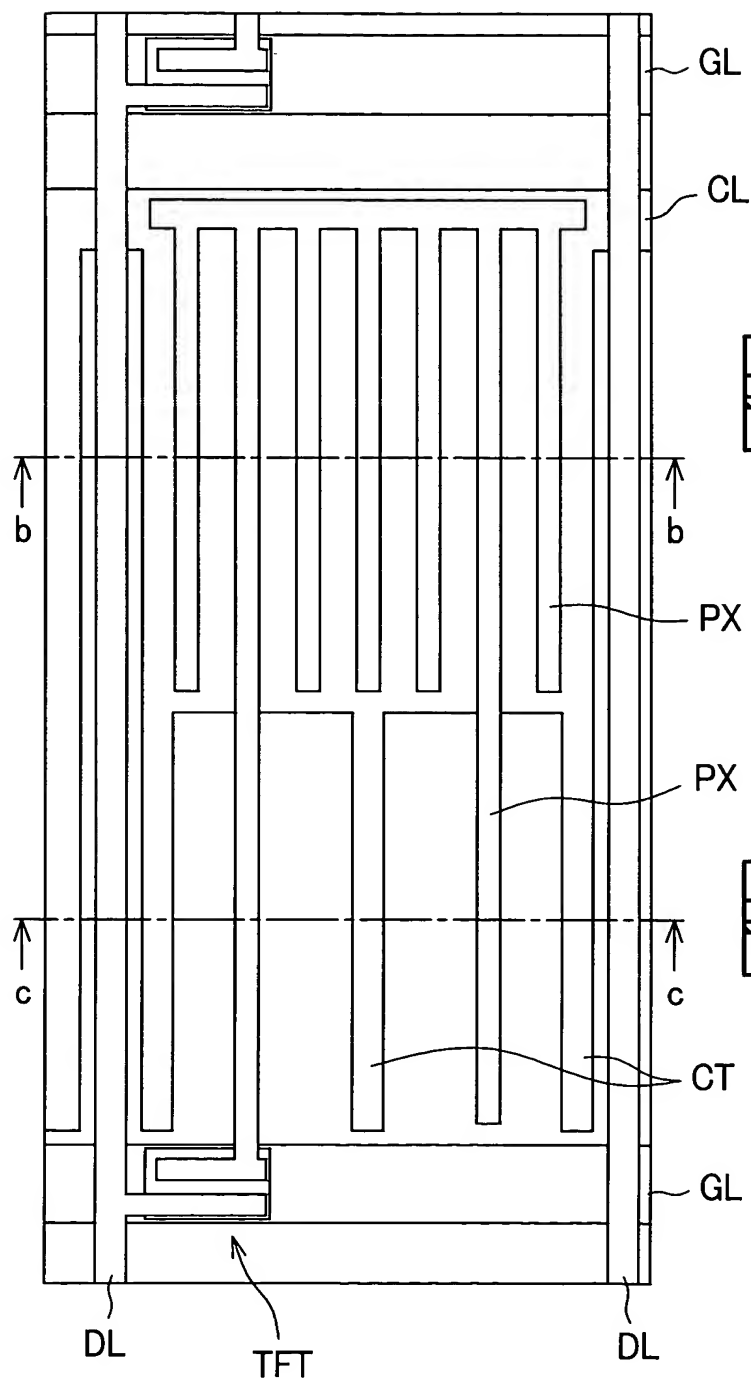
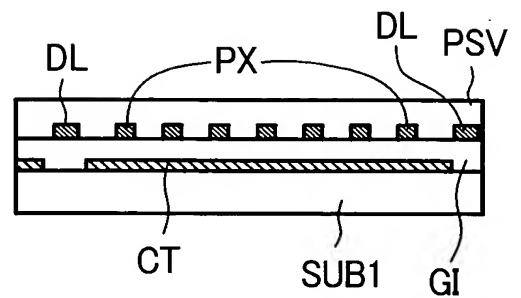


Diagram illustrating a plan view of a thin film transistor (TFT) array substrate. The structure includes a central area with two rows of vertical elements, labeled PX and CT. A central horizontal line is labeled Cst. The entire array is surrounded by a border labeled DL. The top and bottom edges are labeled GL. Dimensions b and c are indicated on the left and right sides. A TFT label points to a specific structure at the bottom left.

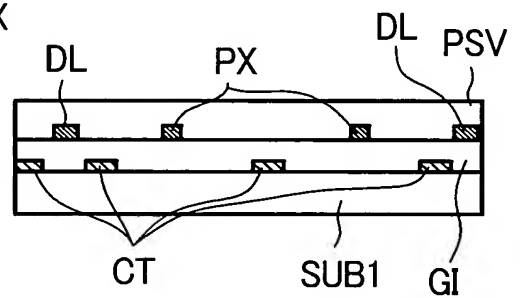
*FIG. 6A*



*FIG. 6B*

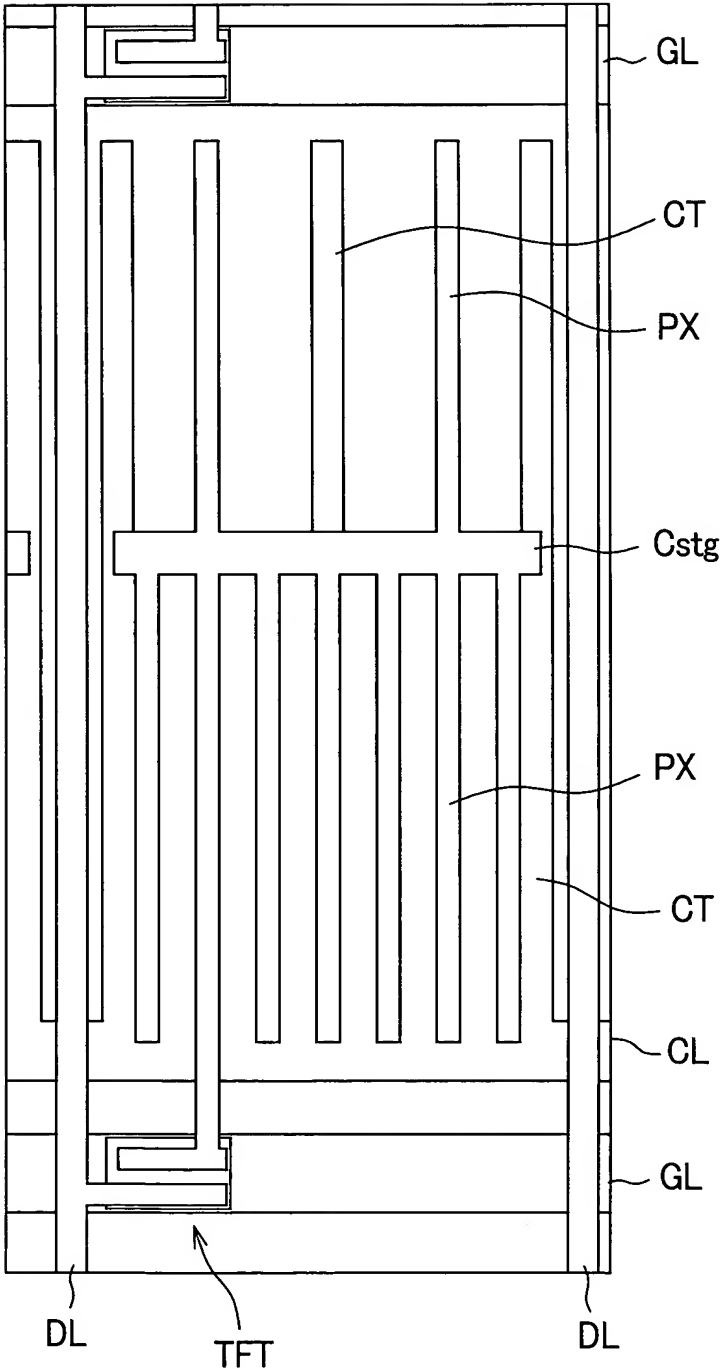


*FIG. 6C*



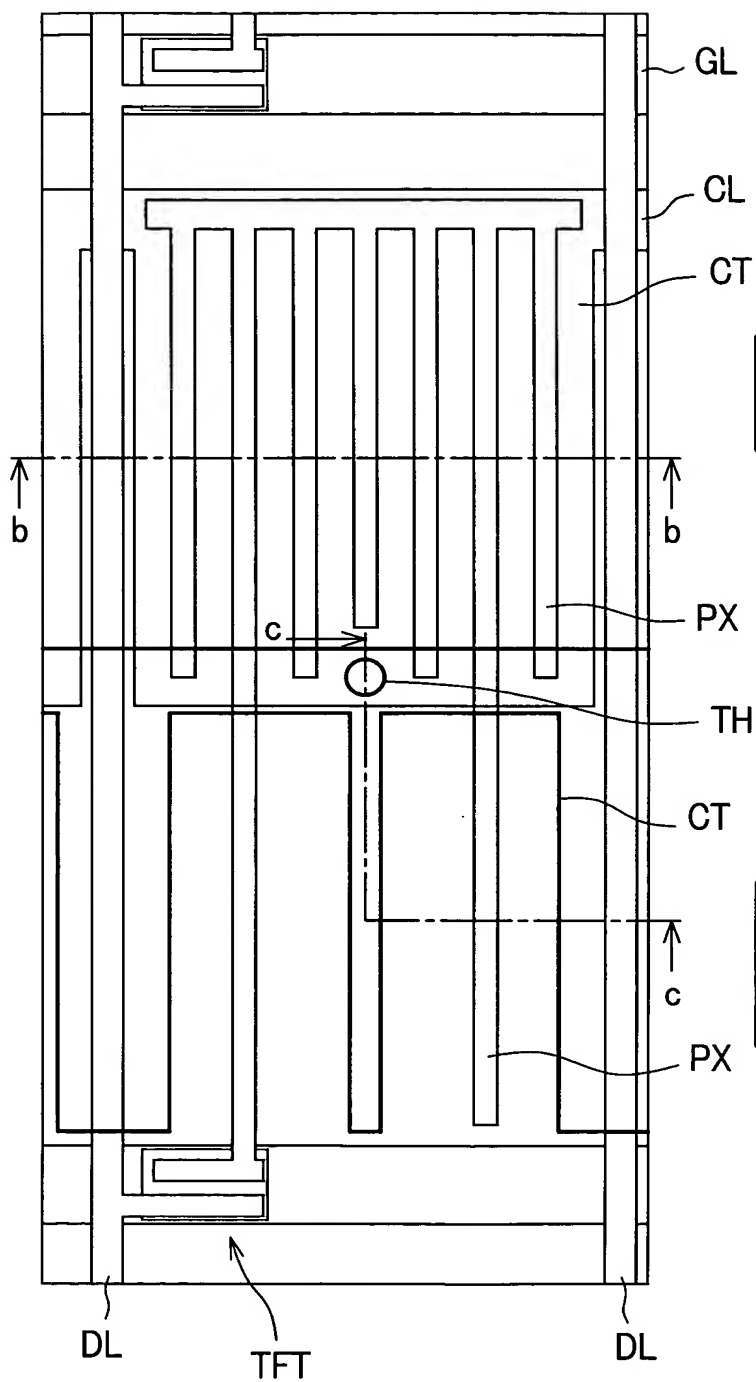


*FIG. 8*

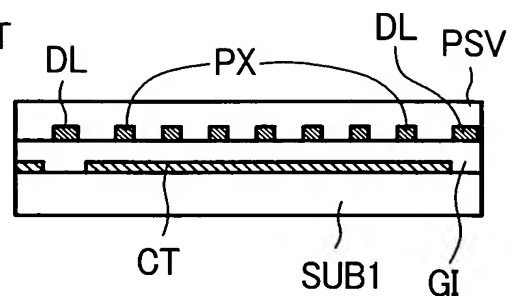




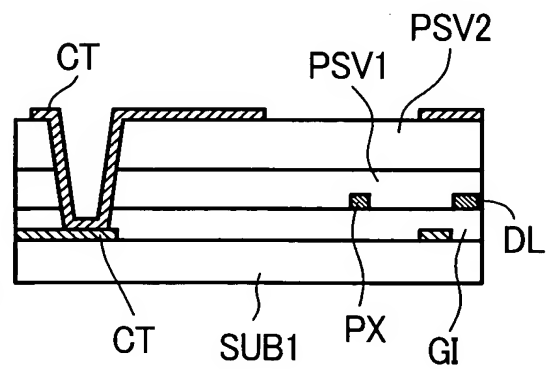
*FIG. 9A*



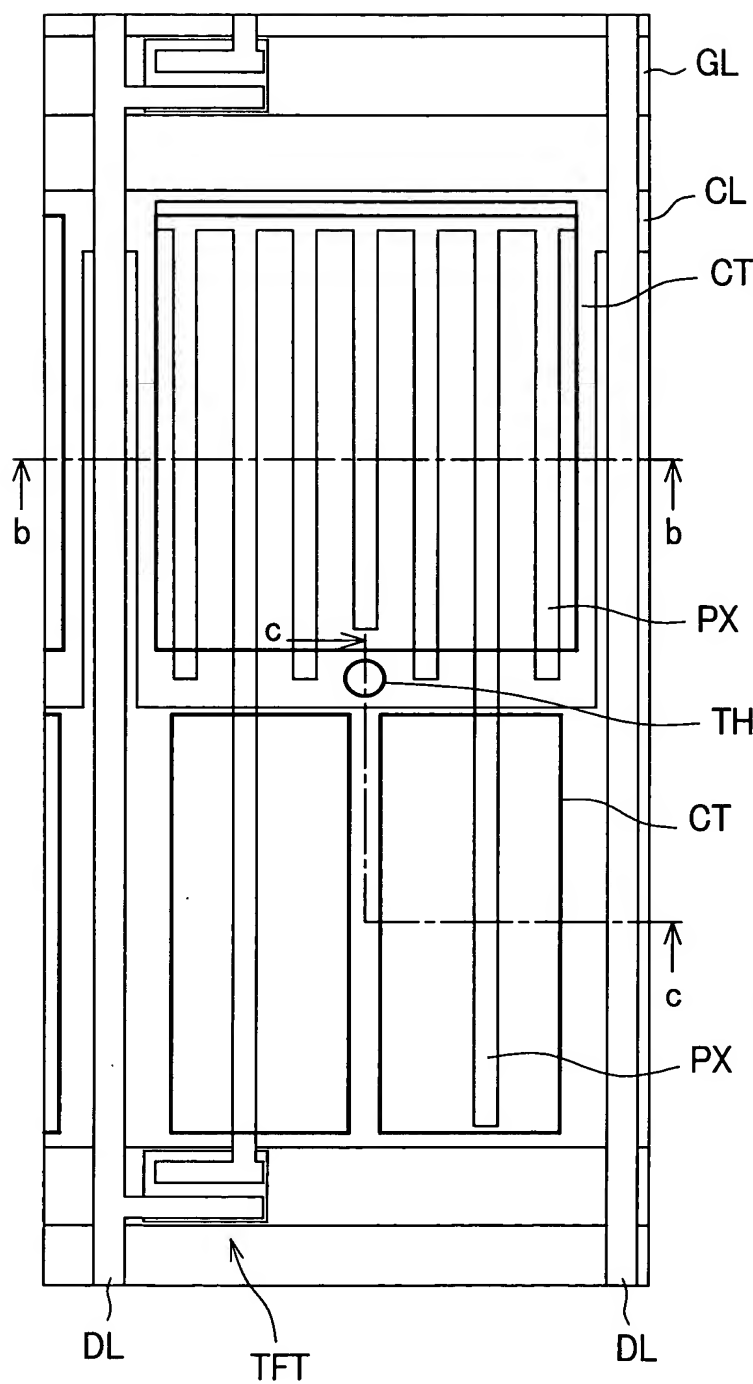
*FIG. 9B*



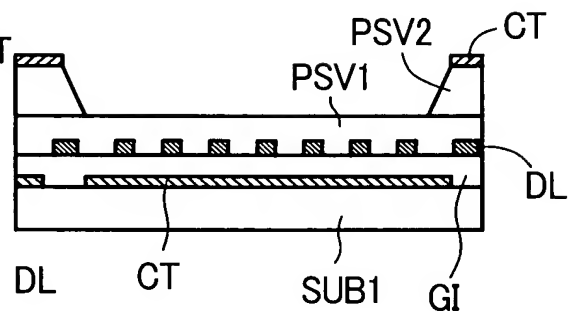
*FIG. 9C*



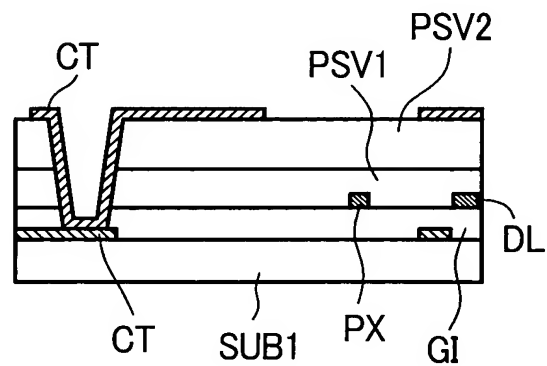
*FIG. 10A*



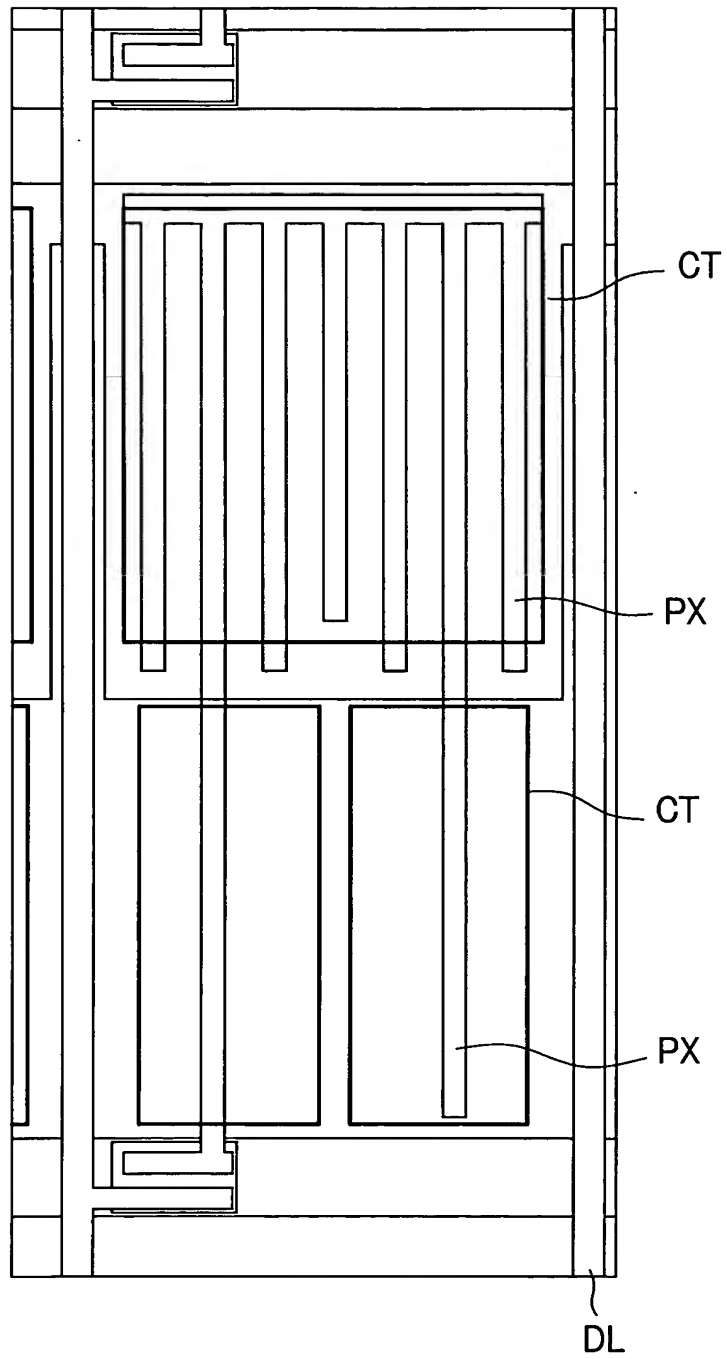
*FIG. 10B*



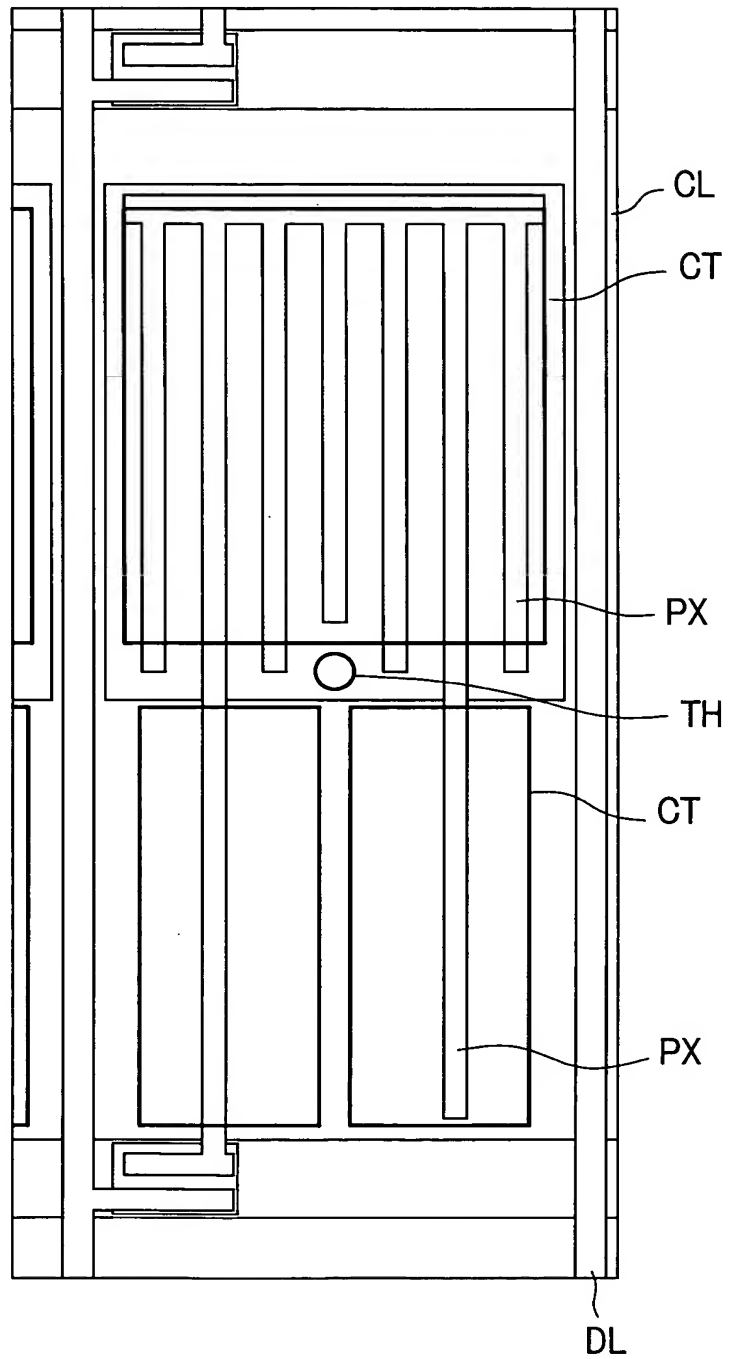
*FIG. 10C*



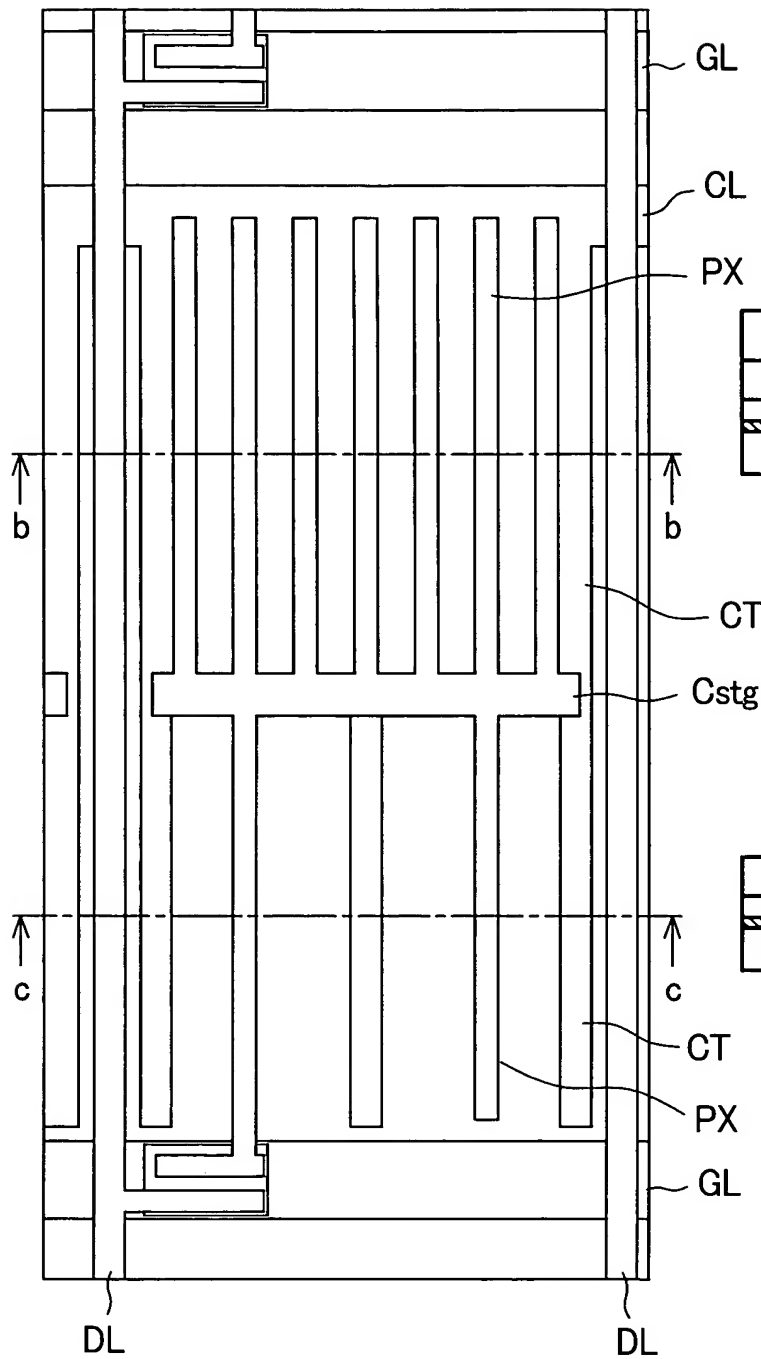
*FIG. 11*



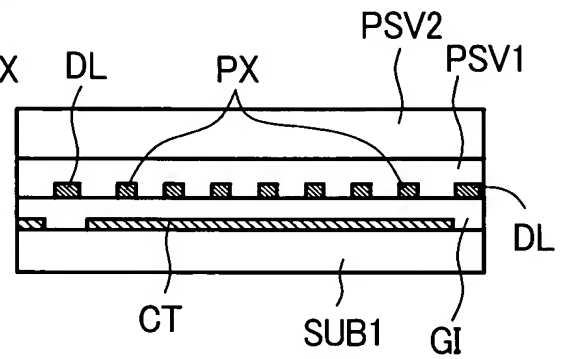
*FIG. 12*



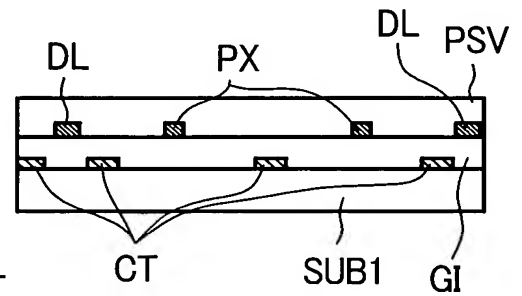
*FIG. 13A*



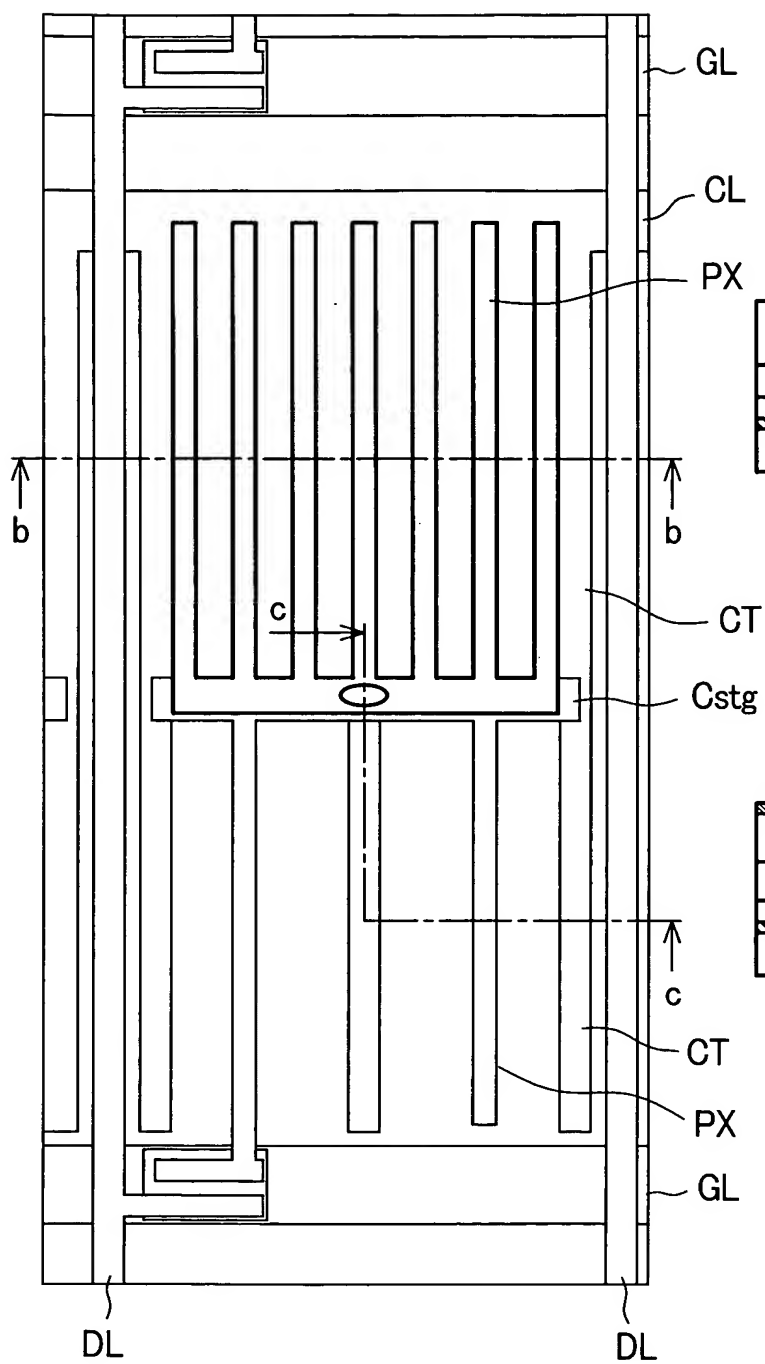
*FIG. 13B*



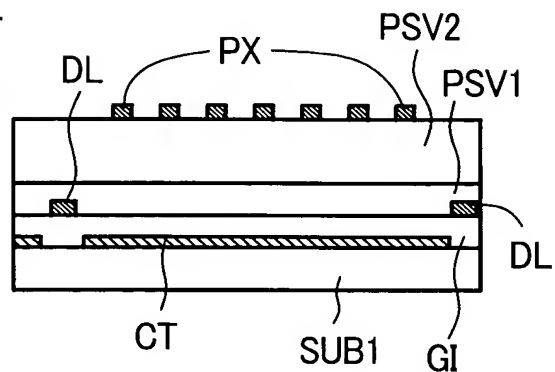
*FIG. 13C*



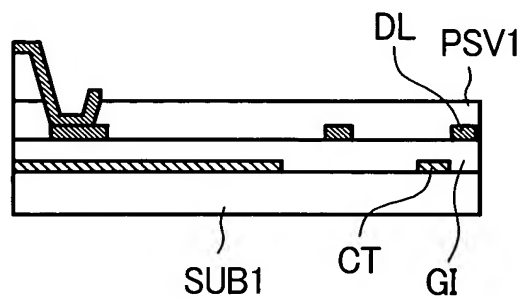
*FIG. 14A*



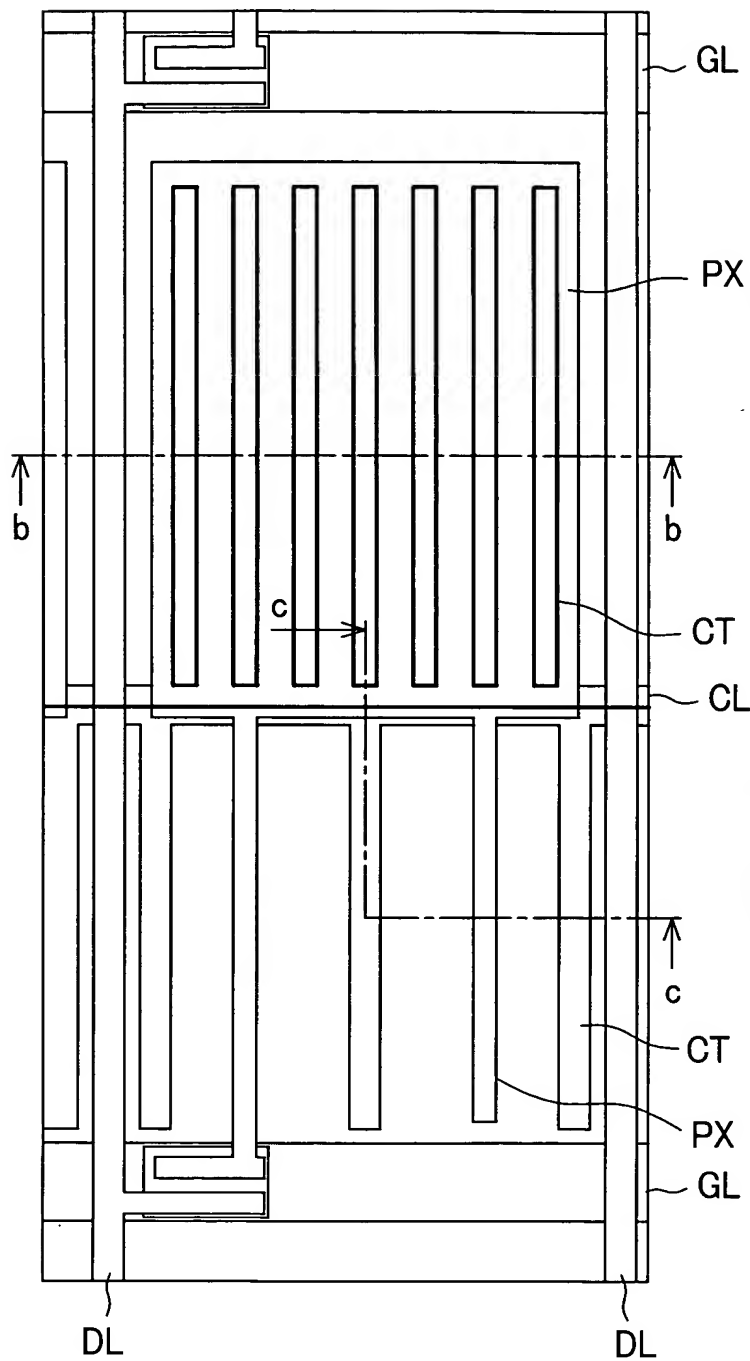
*FIG. 14B*



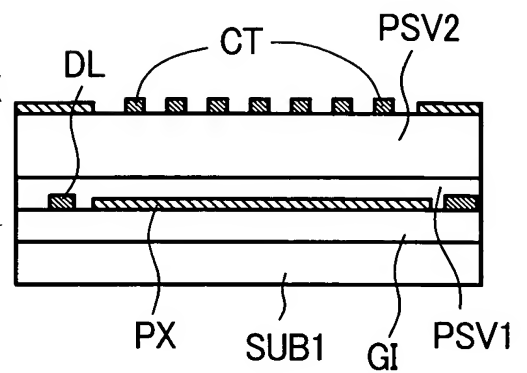
*FIG. 14C*



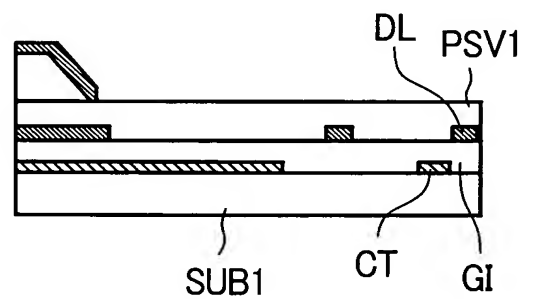
*FIG. 15A*



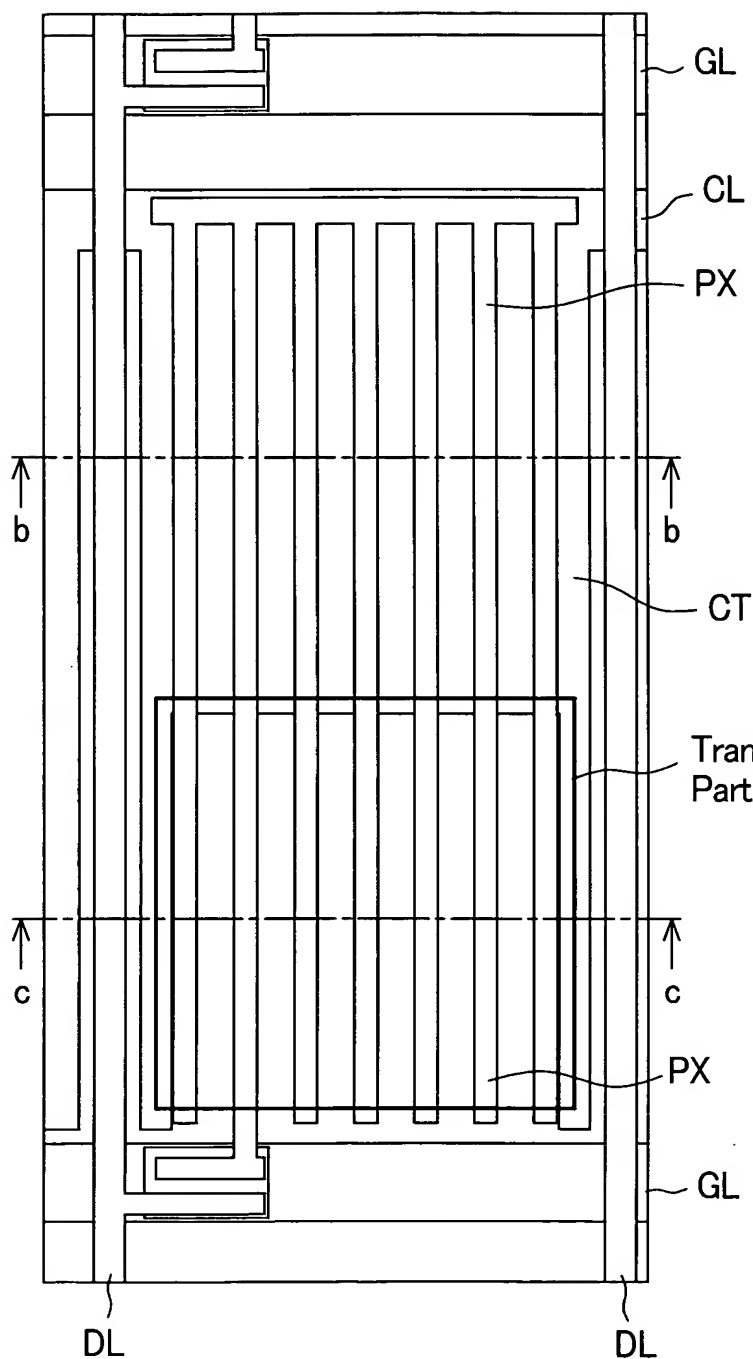
*FIG. 15B*



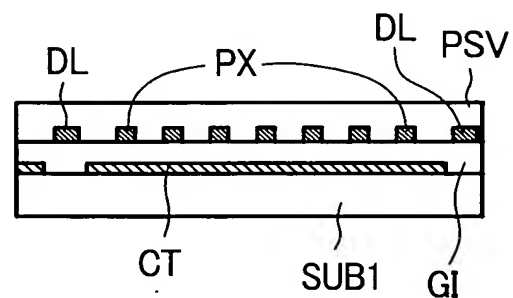
*FIG. 15C*



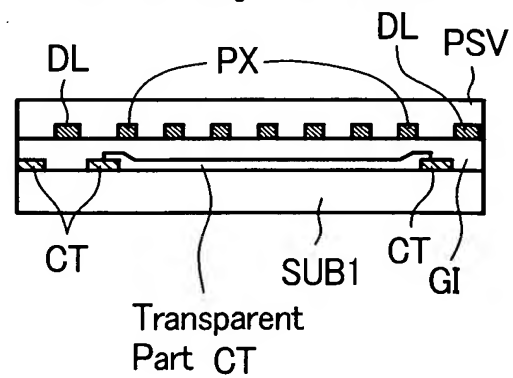
*FIG. 16A*



*FIG. 16B*

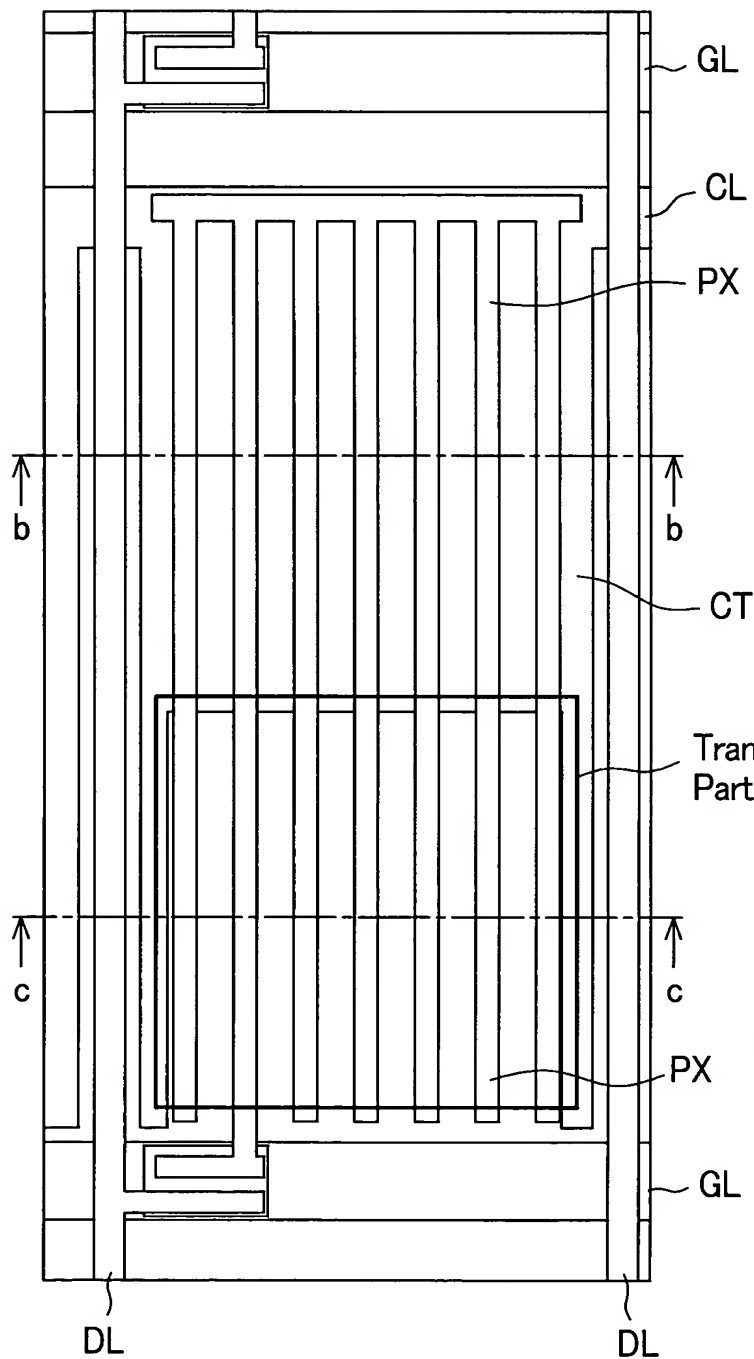


*FIG. 16C*

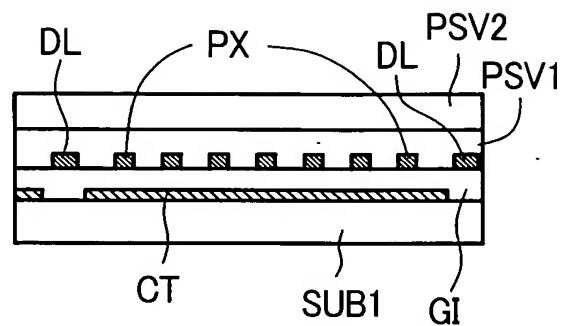




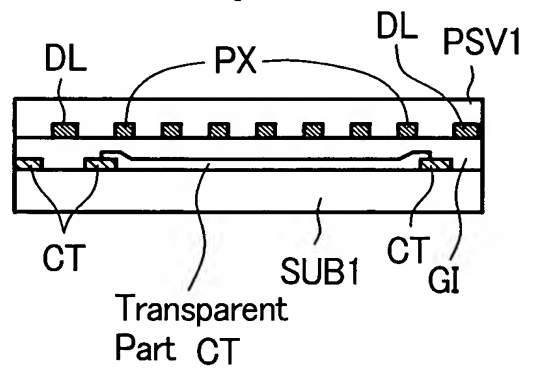
*FIG. 17A*



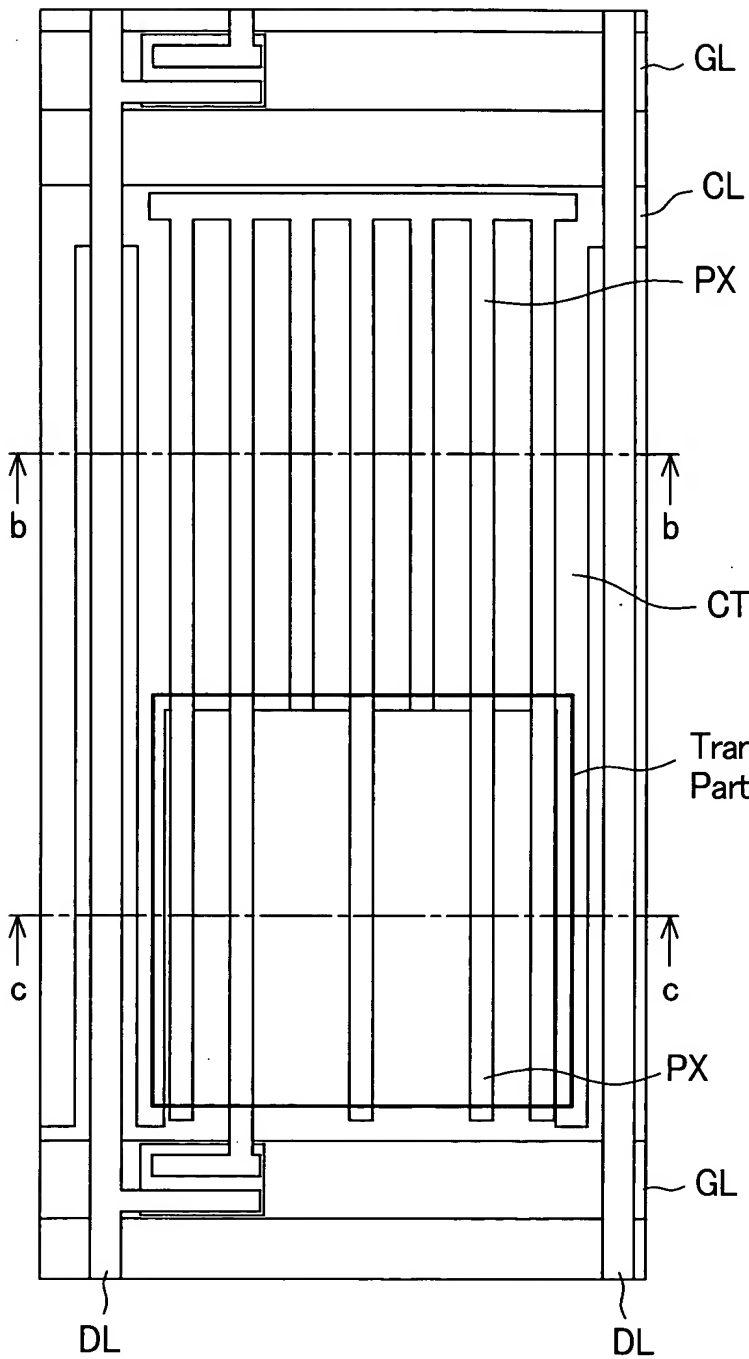
*FIG. 17B*



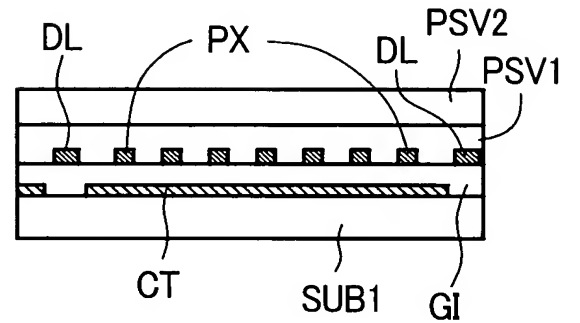
*FIG. 17C*



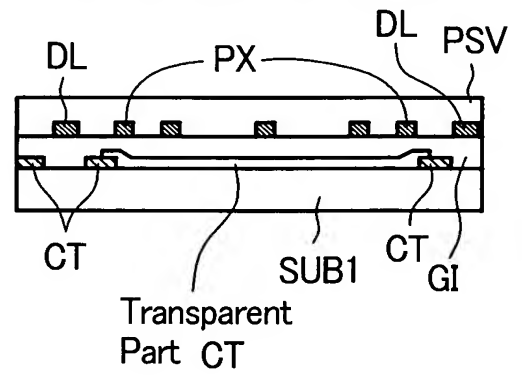
*FIG. 18A*



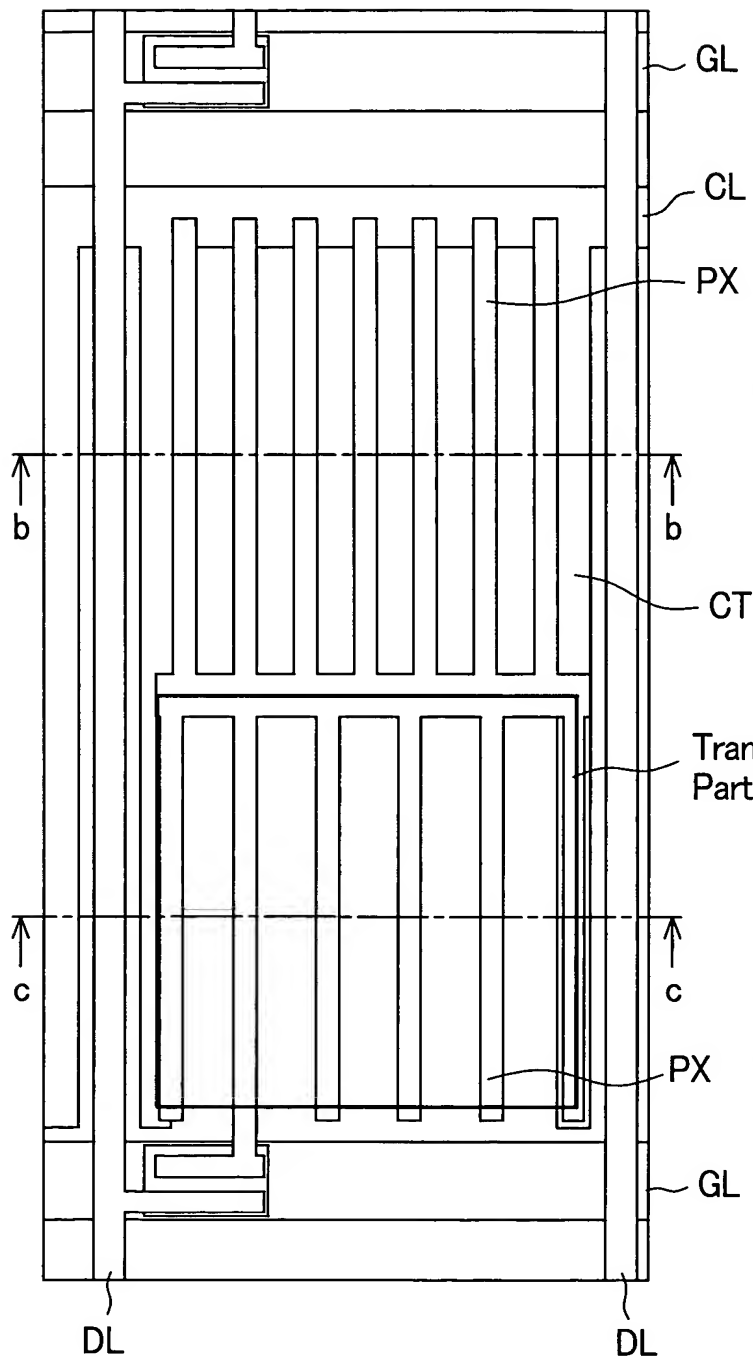
*FIG. 18B*



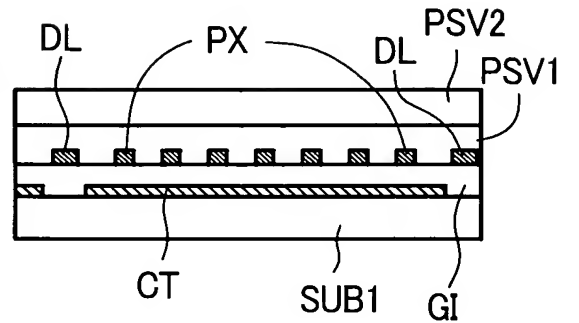
*FIG. 18C*



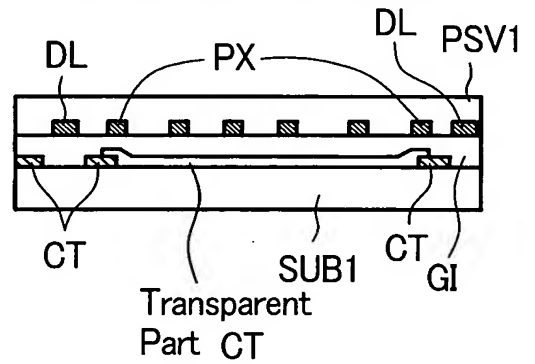
*FIG. 19A*



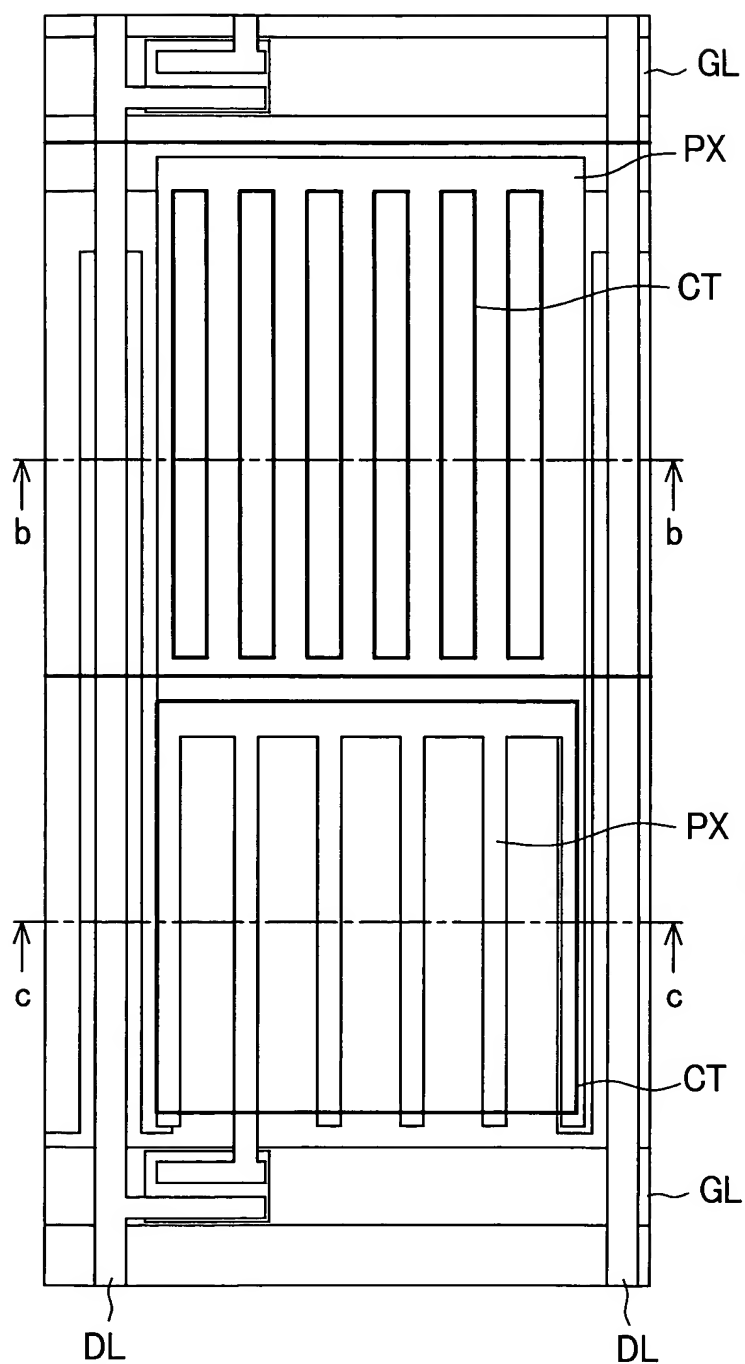
*FIG. 19B*



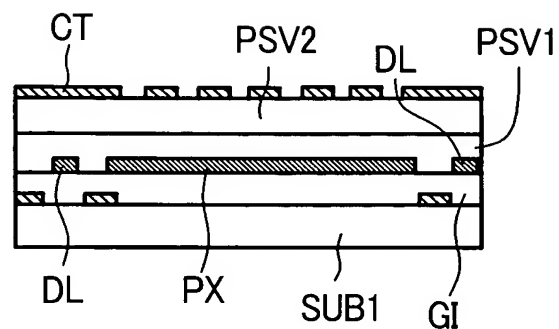
*FIG. 19C*



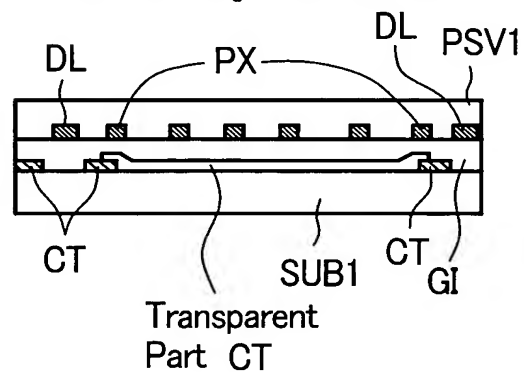
*FIG. 20A*



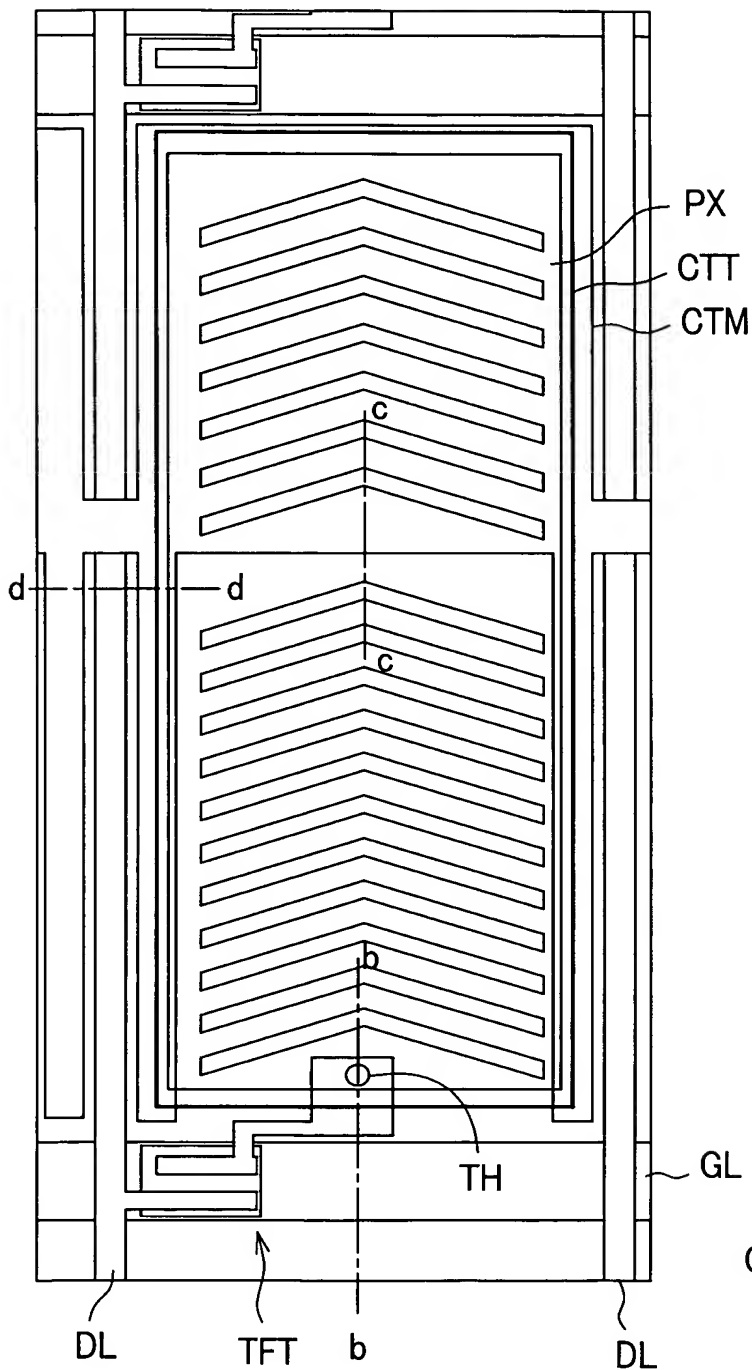
*FIG. 20B*



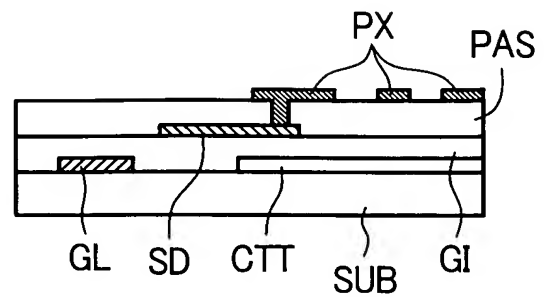
*FIG. 20C*



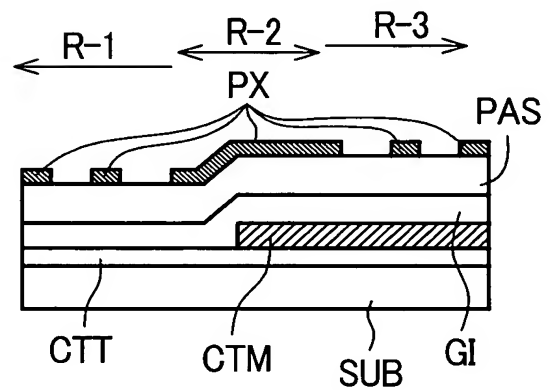
*FIG. 21A*



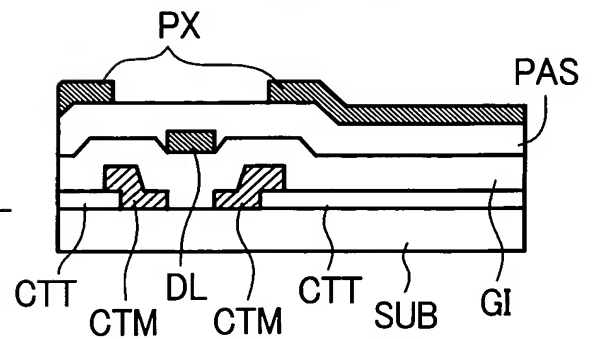
*FIG. 21B*



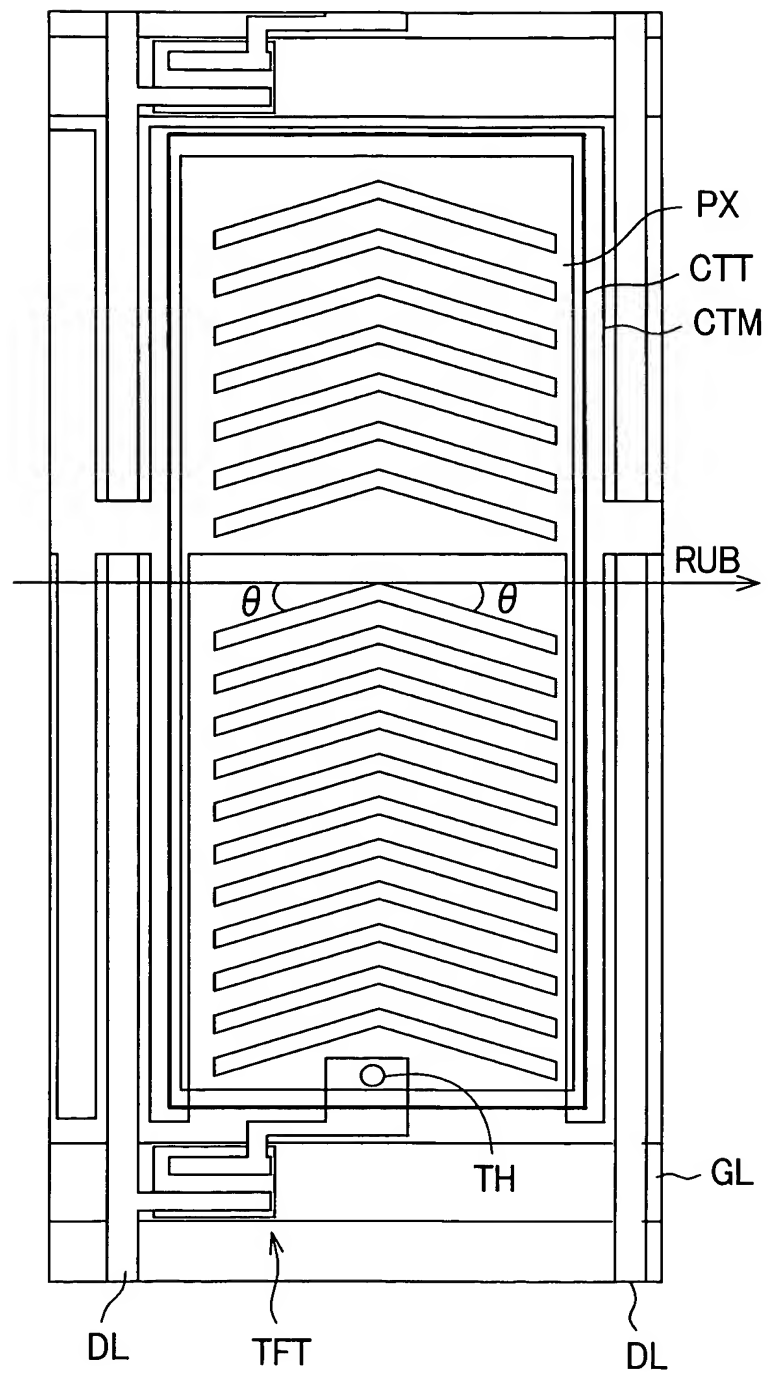
*FIG. 21C*



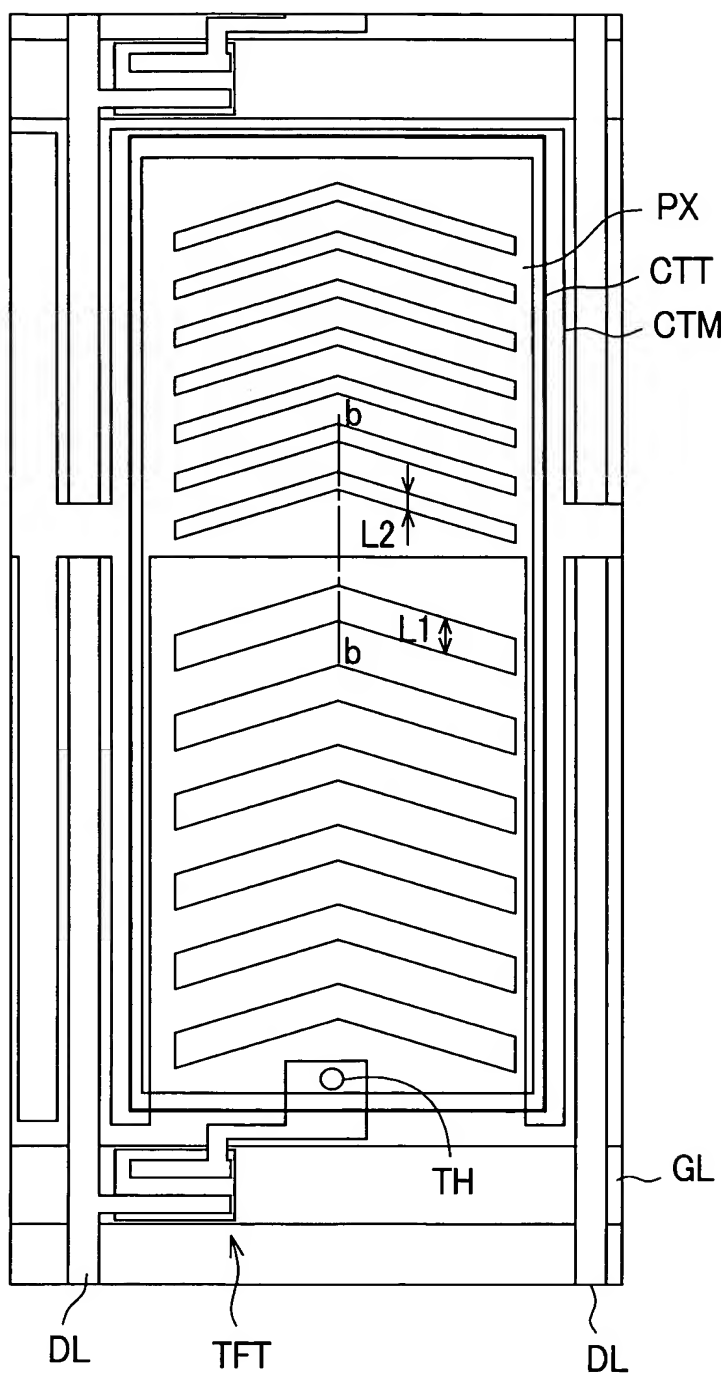
*FIG. 21D*



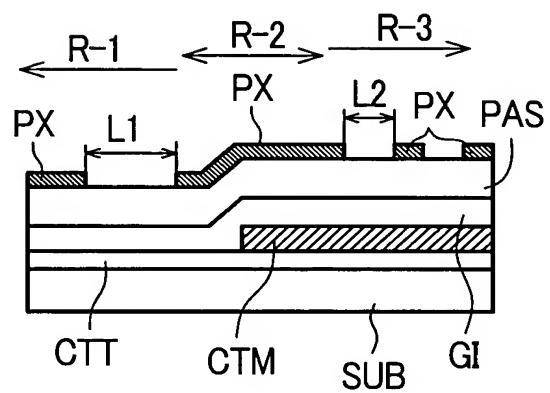
*FIG. 22*



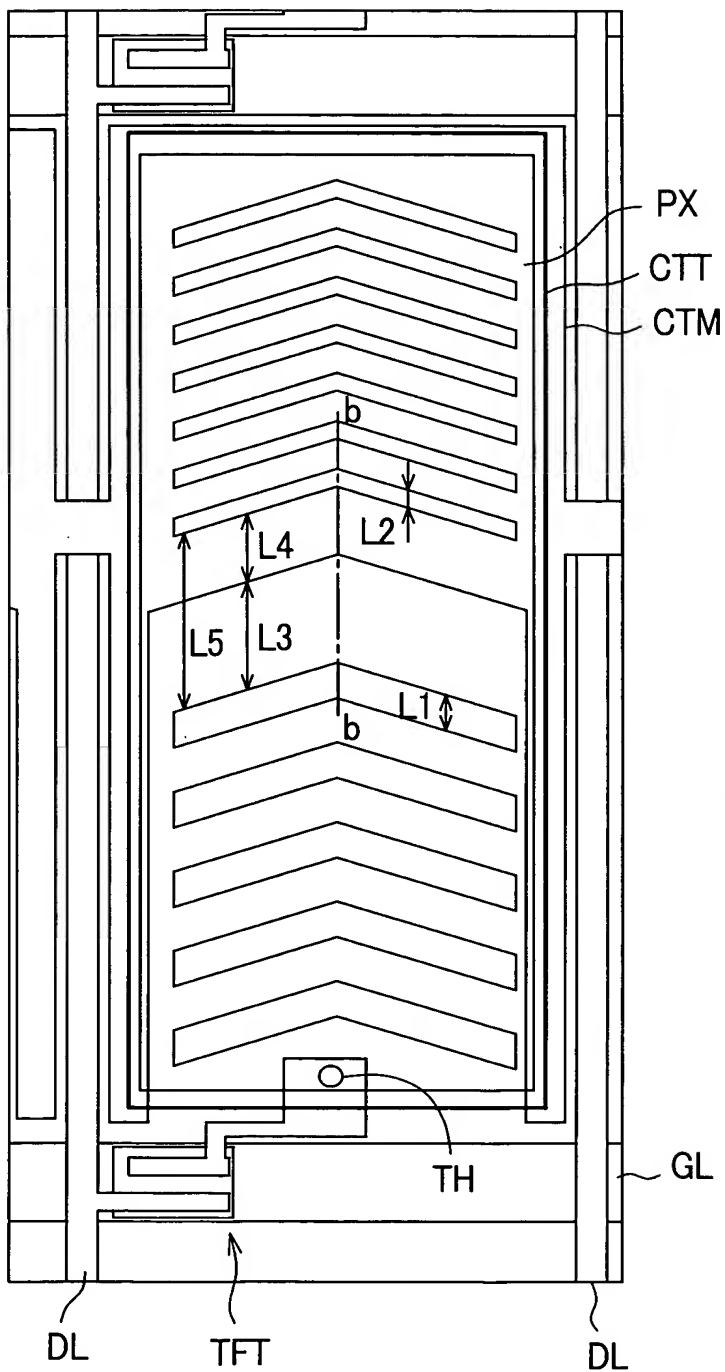
*FIG. 23A*



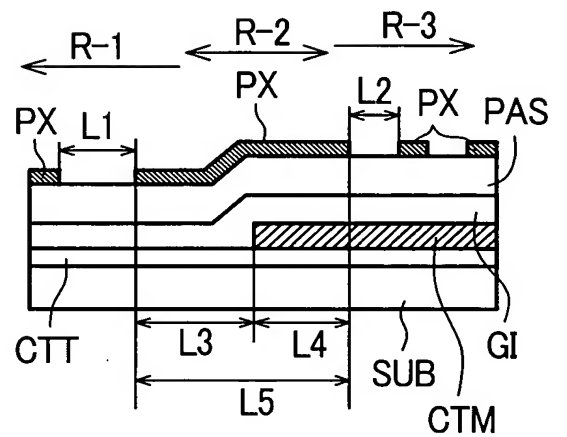
*FIG. 23B*



*FIG. 24A*

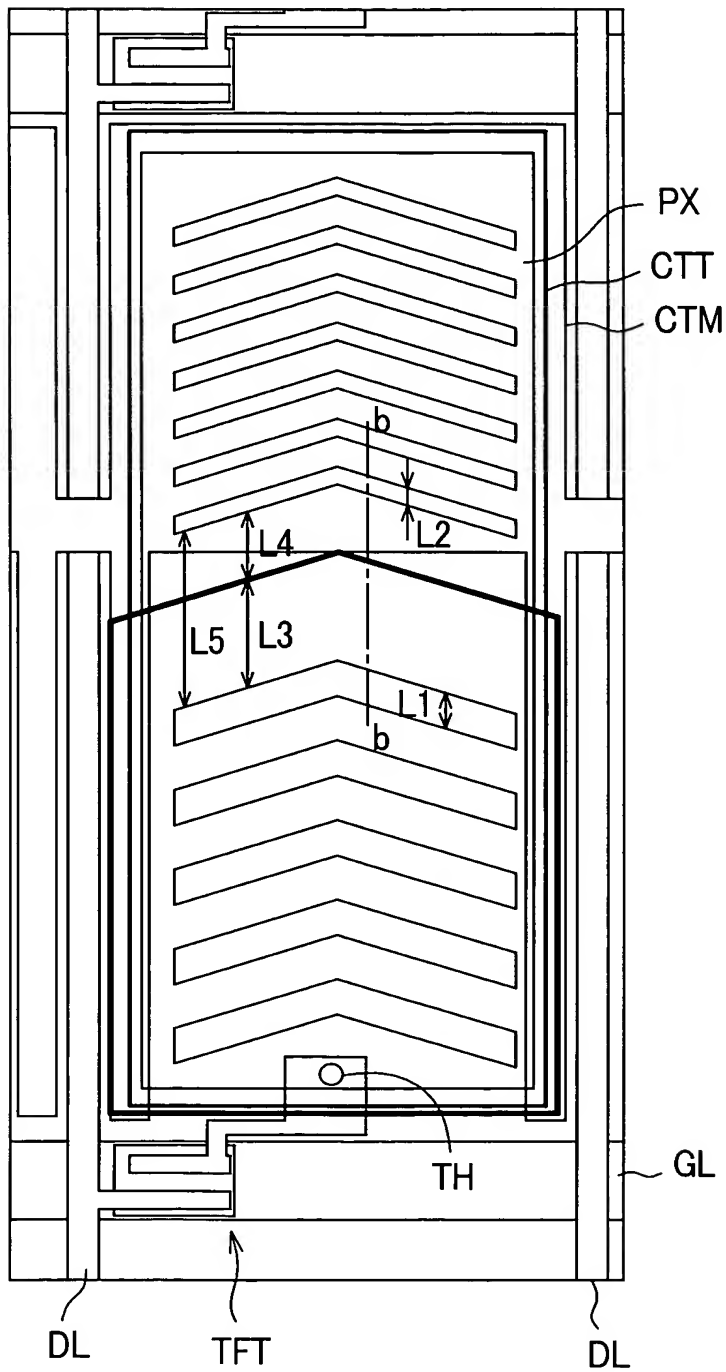


*FIG. 24B*

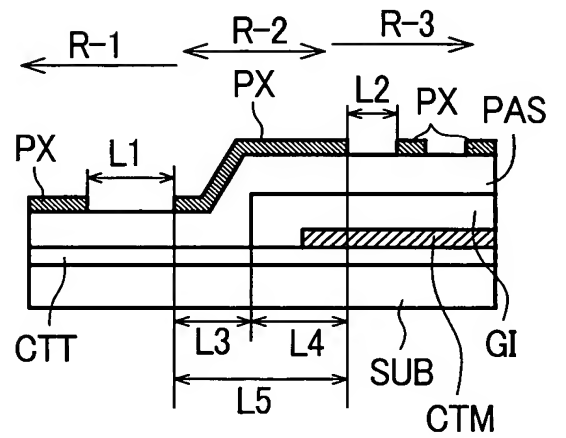




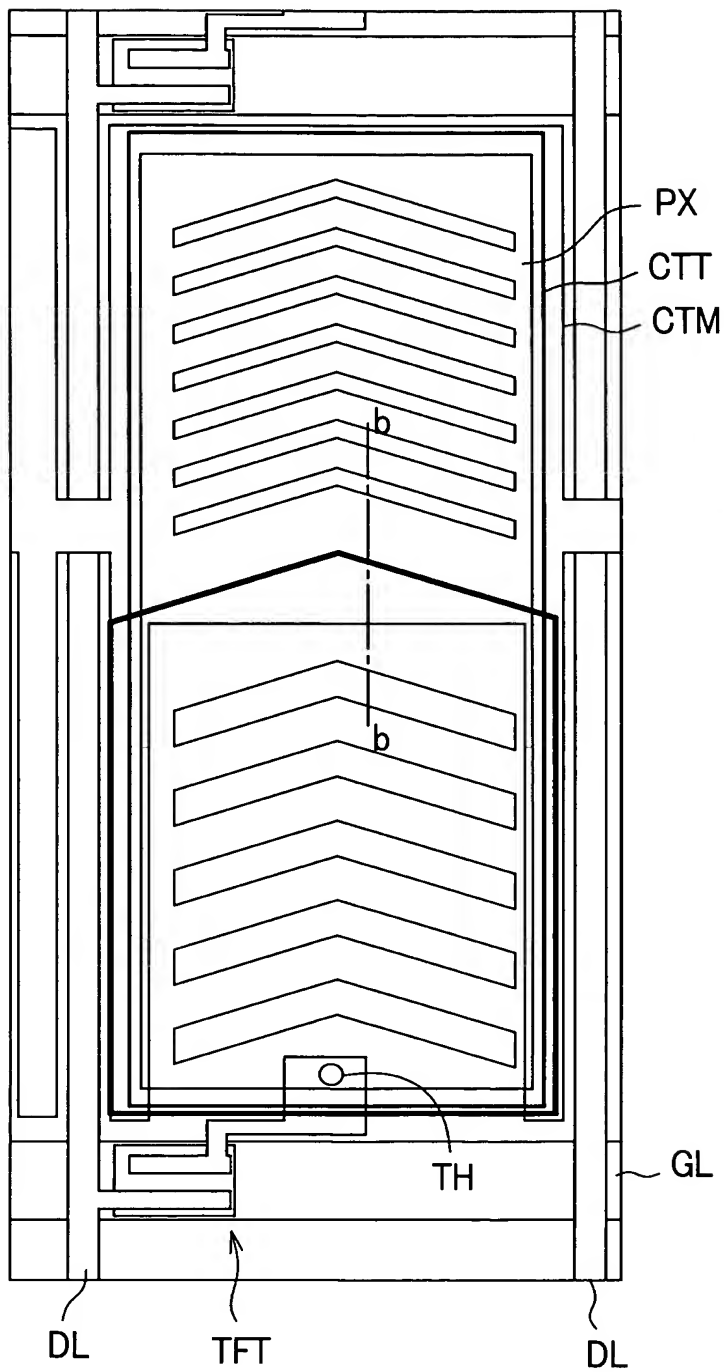
*FIG. 25A*



*FIG. 25B*



*FIG. 26A*



*FIG. 26B*

